EMBARGOED UNTIL: Tuesday, 17 November 2026, 5:00 PM MYT (Malaysia Time)

SKYECHIP AND SIPEARL SIGN MOU TO JOINTLY DEVELOP NEXT-GENERATION ARM NEOVERSE CSS COMPUTE PLATFORM

- *The agreement was signed on the sidelines of the visit of the President of the French Republic, Emmanuel Macron, to Malaysia, in the presence of President Macron and Dato' Seri Anwar Ibrahim, Prime Minister of Malaysia.*

KUALA LUMPUR, 17 NOVEMBER 2026 – SkyeChip Berhad ("**SkyeChip**" or "**the Company**") (Bursa Malaysia: SKYECHIP, Stock Code: 5357), a Malaysia-headquartered global integrated circuit ("**IC**") design company specialising in the design and development of silicon intellectual property ("**IP**") and silicon products, and SiPearl SAS ("**SiPearl**"), a France-based European fabless designer of high-performance energy-efficient CPUs built for sovereign supercomputing, AI and data centres, today announced that they signed a non-binding Memorandum of Understanding ("**MoU**") to jointly define, develop and commercialise a next-generation compute platform based on Arm Neoverse Compute Subsystems ("**CSS**").

The collaboration is intended to be undertaken through a joint venture company ("**JV Company**") to be incorporated in Malaysia, targeted for December 2026, with SkyeChip expected to hold a majority stake of 51% and SiPearl the remaining 49%.

The parties intend to apply, through the JV Company, to the relevant Malaysian authorities for the Arm Neoverse CSS licence. Subject to the grant of the licence, the parties intend to jointly define the platform specification, after which silicon development, tape-out and post-silicon activities are to be carried out through the JV Company, with the aim of progressing towards production and commercialisation, subject to the necessary approvals and successful completion of the development activities.

This collaboration is intended to combine SiPearl's system-level architecture and server CPU expertise with SkyeChip's portfolio of silicon IP, including high-bandwidth memory interface, Network-on-Chip (NoC) and die-to-die (D2D) interface IP, to enable a

differentiated processor and system solution with accelerated development, lower cost and wider market access.

The incorporation of the joint venture and the progression of the cooperation remain subject to the execution of definitive agreements which the parties intend to negotiate within 90 days from the date of the MoU, and the approval of the respective Boards of Directors of both companies, as well as applicable regulatory and export control clearances, including the grant of the Arm Neoverse CSS licence.

"*We are proud to partner with SiPearl on developing a next-generation compute platform. Through this partnership, we aim to accelerate development, optimise costs and widen our market reach across Asia, North America and Europe. We believe this collaboration will contribute to the growing semiconductor ecosystems in Malaysia and Europe, particularly in advanced IC design and high-performance computing,*" said **Dato' Fong Swee Kiang**, Chief Executive Officer & Founder of SkyeChip Berhad.

"The release of our first-generation CPU dedicated to supercomputing and AI, Rhea1, which has been "right the first time", has strengthened the position of SiPearl as a global leader specializing in Arm technology. We are thrilled to leverage this expertise with our Malaysian partner SkyeChip. Together, we will contribute to support the strong expected growth of the Arm-based CPUs' market. Furthermore, in a complex geopolitical context, we aim to offer a powerful solution to regions around the world seeking resilient, high-performance, and secure CPUs." said **Philippe Notton**, CEO & Founder of SiPearl.

About SkyeChip Berhad

SkyeChip Berhad (Bursa Malaysia: SKYECHIP, Stock Code: 5357) is a Malaysia-based global silicon IP and silicon products company. Commencing business in 2020, SkyeChip's silicon IP portfolio includes high-bandwidth memory interface IP, Network-on-Chip IP and D2D interface IP, supporting applications across high-performance computing and AI. SkyeChip also offers silicon product development capabilities, spanning architecture, design and verification through to prototyping, qualification and high-volume production enablement. SkyeChip has developed over 120 patent inventions, filed across Malaysia, China and the United States. For more information, visit www.skyechip.com.

About… SiPearl

SiPearl is the European fabless designer of secure high-performance energy-efficient CPUs built for sovereign HPC, AI and data centre. These CPUs will help address strategic challenges in the fields of security, defence, medical research, energy, climate and engineering with a reduced environmental footprint.

Featuring 80 Arm Neoverse V1 cores with 61 billion transistors, SiPearl's first-generation CPU, Rhea1, will be released end of 2026. Sipearl's CPUs will equip the two first European exascale supercomputers: Rhea1 will be integrated into the JUPITER machine based in Germany and Rhea2 will be part of Alice Recoque in France.

Supported by the European Union and France, SiPearl employs 200 people in France, Spain, Italy and Taiwan.

www.sipearl.com



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Media Enquiries:

SkyeChip Berhad
Email: corporate@skyechip.com

SiPearl
Marie-Anne Garigue, Head of Communications: +33 6 09 05 87 80 – marie-anne.garigue@sipearl.com
Grégory Bosson, Senior Communication Officer: +33 6 60 75 71 61 – gregory.bosson@sipearl.com

Impact Advisory Sdn Bhd
Alex Te
Tel: +60 17 408 7243
Email:
alex.te@impactadvisory.com.my